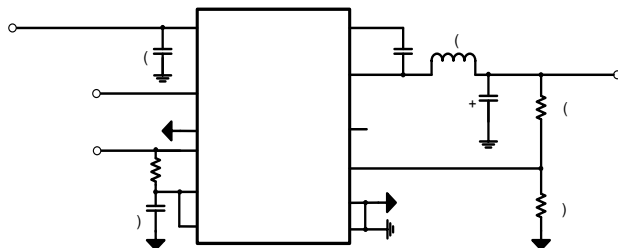


$\frac{V_{OUT}}{V_{IN}} \times 100\%$ (/)
 $\frac{V_{OUT}}{V_{IN}} \times 100\%$)
 ()
 (, & &)
 (, & %
 +
 (+

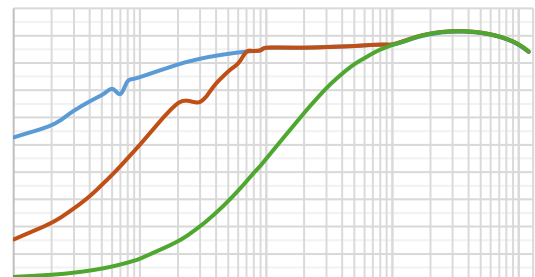
The SCT22C1C adopts a Constant On-Time (COT) control to provide fast transient response and easy loop stabilization. The device offers fixed 1ms soft start to prevent inrush current during the startup of output voltage ramping. Power Good with open drain output signals that the output voltage is within regulation.

The SCT22C1C has the MODE pin to select Pulse Frequency Modulation (PFM) operation mode to achieve the light load power save, or Ultrasonic Mode (USM) to keep the switching frequency above audible frequency areas during light-load or no-load conditions, or Forced Continuous Conduction Mode (FCCM) to achieve the small output ripple.

□
 %
 ()
 %



2.9V-18V, Synchronous Buck Converter



$V_{IN}=12V$, $V_{OUT}=1.2V$, $f_{sw}=400kHz$

(% %
(% %
(% & %

ORDERABLE DEVICE	PACKAGING TYPE	STANDARD PACK QTY	PACKAGE MARKING	PINS	PACKAGE DESCRIPTION	MSL
)) (	□	,	) (	()	) ()	(

DESCRIPTION	MIN	MAX	UNIT
	%	(	
	%	" -	
	(	" %	
	%	-	
	%	-	
	%	-	
	%	-	
	%	-	
)	+	(,	
	- ,	(,	

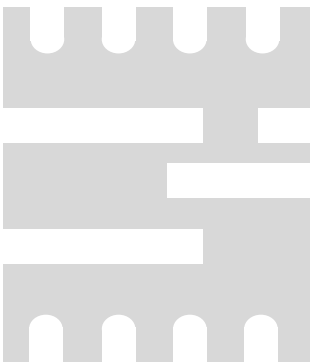


Figure 1. 12-Lead Plastic QFN)

(%
) % (,
% %

NAME	NO.	PIN FUNCTION
	(	% % % %)) (% %)
	)	% %
		% ,
	+	%
	,	% (+ %

	-	%	%
	.	%	%
	/	%	%
	(	%	%
	((	Feedback voltage Input. Connect FB to the tap of a resistor divider from output voltage to AGND to set up output voltage. The device regulates FB to the internal reference value of 0.6V typical.	
	()	%	%

PARAMETER	DEFINITION	MIN	MAX	UNIT
		) %	(/	
		%	)	
		+	() ,	

PARAMETER	DEFINITION	MIN	MAX	UNIT
	(	)	"	
	(+	%	" %	
(	(, ,			%
)	(, -			%

PARAMETER	THERMAL METRIC	QFN-12L	UNIT
	(	-) %	&
	(	. %	
	(	) %	
	(	+ %/	
	(	+ %+	
	)	/ %	
	)	%	

SCT22C1C

		)	. %	
(	, (. + %			%
)		)) (	(	%
			- + %	

(	+	(),				%
SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT

Power Supply and Output

			) %		(/	
			) %	) %	) %	
				)		
			(	,		
		) % ,		(,	) ,	
		%	+%	+%	,	
		,			%	

Reference and Control Loop

		) % (/		% +	%	% -	
		(%				(,	

Power MOSFETs

		,		(,		
		,		,		

Enable and Soft-start

			(%	(%	(%	
			(%	(%		
		(	(	(%	)	
		(		(	)	

Operation Mode

		,	+%			
			(%		%	
					(	

Switching Frequency

			+	+	+ -	
				-		
				(		

Power Good

			,			
			/ ,			
			((,			
			(,			
			%			
		+	%			
		,			(	

+

% %

)) +

% %

)) (

SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Protection						
			(	(+	)	
			) %	,	. %	
				(		
				()		
				((.		
				. ,		
				(,		

(% () (% +) % () (% +

% (% +



Overview

)) ()% (/ () (, %
 , %
)) ((%
 (, , % %
)) ()%) %
 %
 (Ω %
)) (%

Constant on-time (COT) Mode Control

)) (%
 (%
 (%) %
 (%
)) (%

Pulse Frequency Modulation (PFM) and Ultra-sonic Modulation (USM) Modes

)) (%
 %
 %

SCT22C1C

Power Good (PG)

(, , % , % (, , / , %

Bootstrap Voltage Regulator

%,
%,

Over Current Limit and Hiccup Mode

Under-voltage Protection

)) (% . ,)) (%

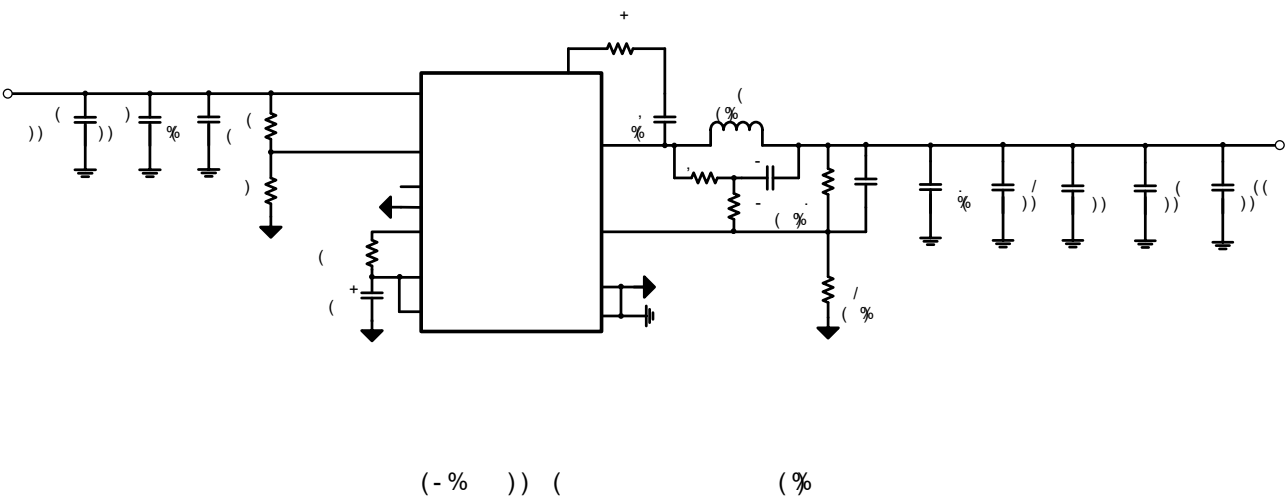
Over voltage Protection

))) ((
% %
%
()
%
%
%
((.

Thermal Shutdown

)) (	(, , °C	%
	(), °C	%

Typical Application



Design Parameters	
Design Parameters	Example Value
	()) % (/
	(%
	()
	+
	)

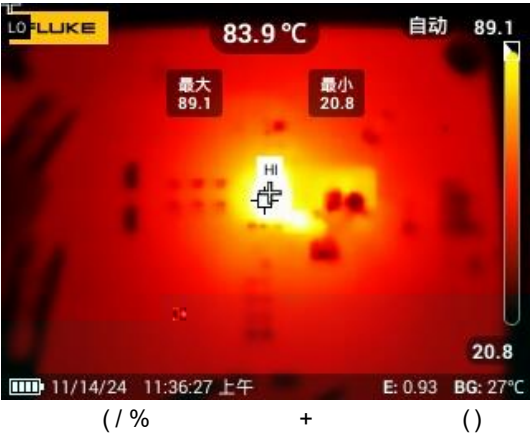
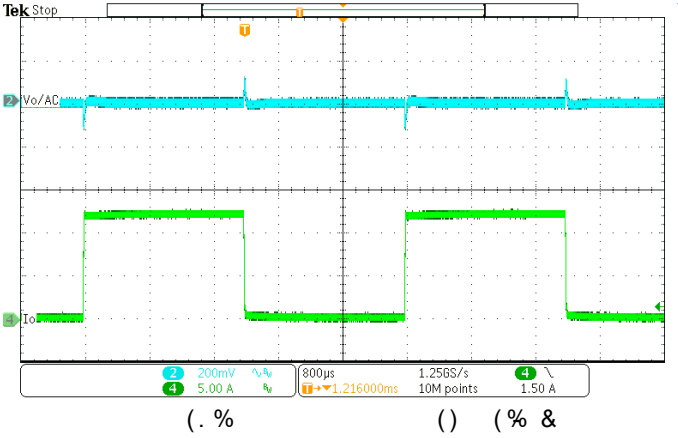
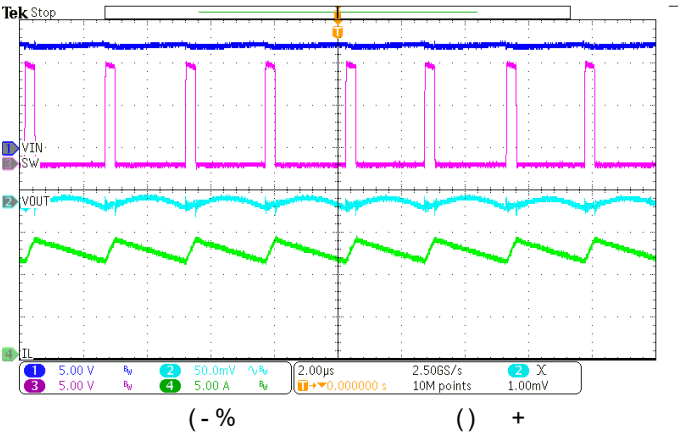
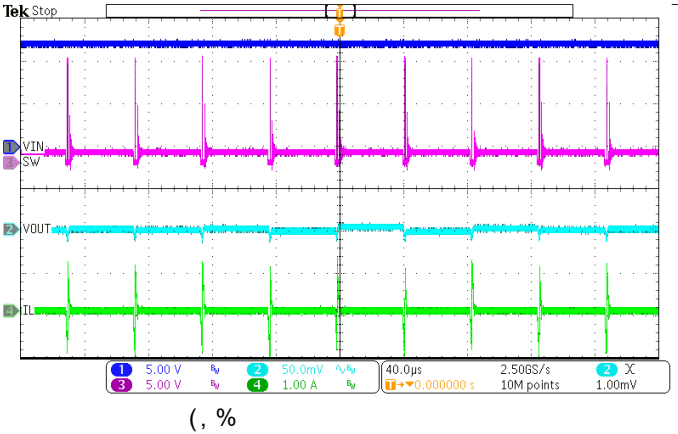
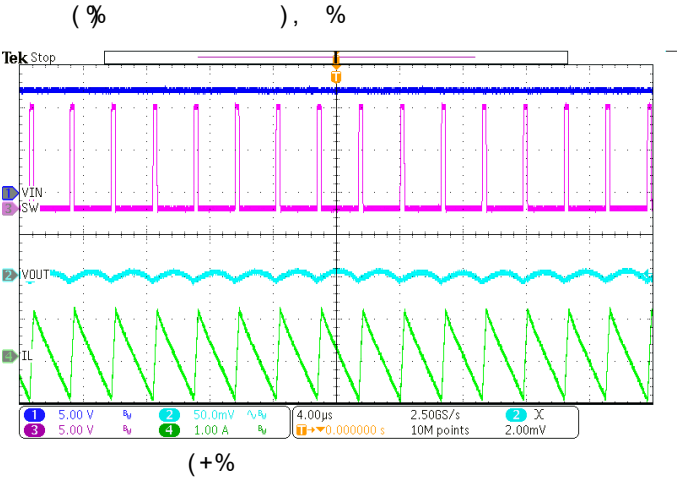
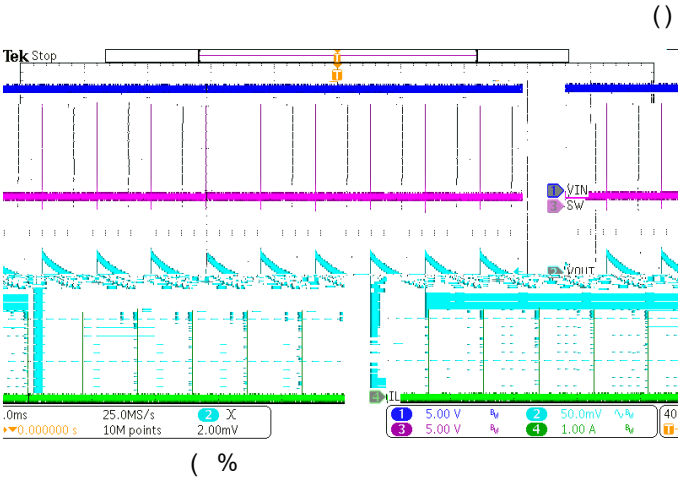
SCT22C1C

Input Capacitor Selection

$$C_{in} = \frac{P_{in}}{f_{sw} \cdot V_{in} \cdot \Delta V_{in}} \cdot \left(\frac{1}{\eta} + 1 \right) \cdot \left(\frac{1}{\eta} + 1 \right) \cdot \left(\frac{1}{\eta} + 1 \right)$$



Application Waveforms(Continued)



SCT22C1C

Layout Guideline

)) (%

%

(% %

) % %

%

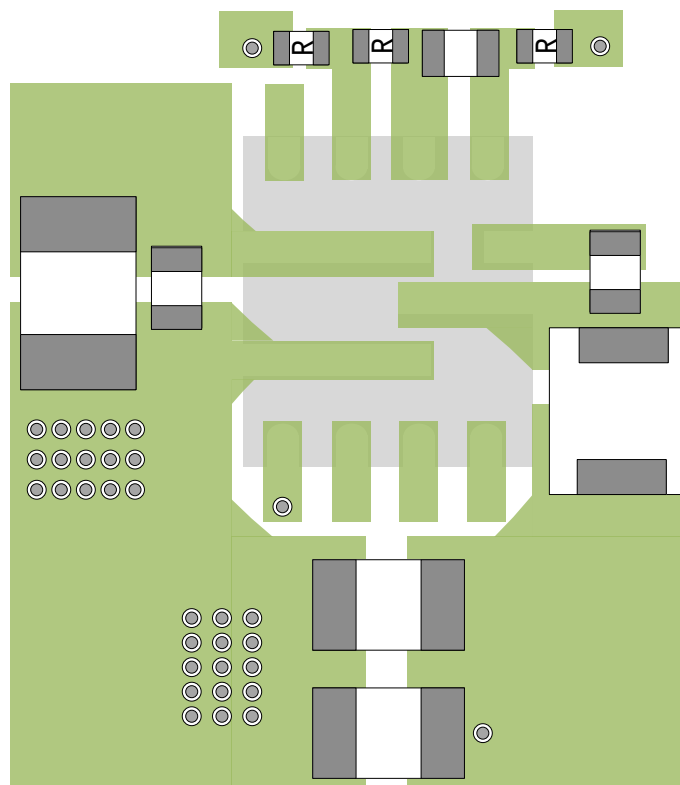
%The bottom layer is a large ground plane connected to the ground plane on top layer by vias. It is recommended 8mil diameter drill holes of thermal vias, but a smaller via offers less risk of solder volume loss. On applications where solder volume loss thru the vias is of concern, plugging or tenting can be used to achieve a repeatable process.

+ % %

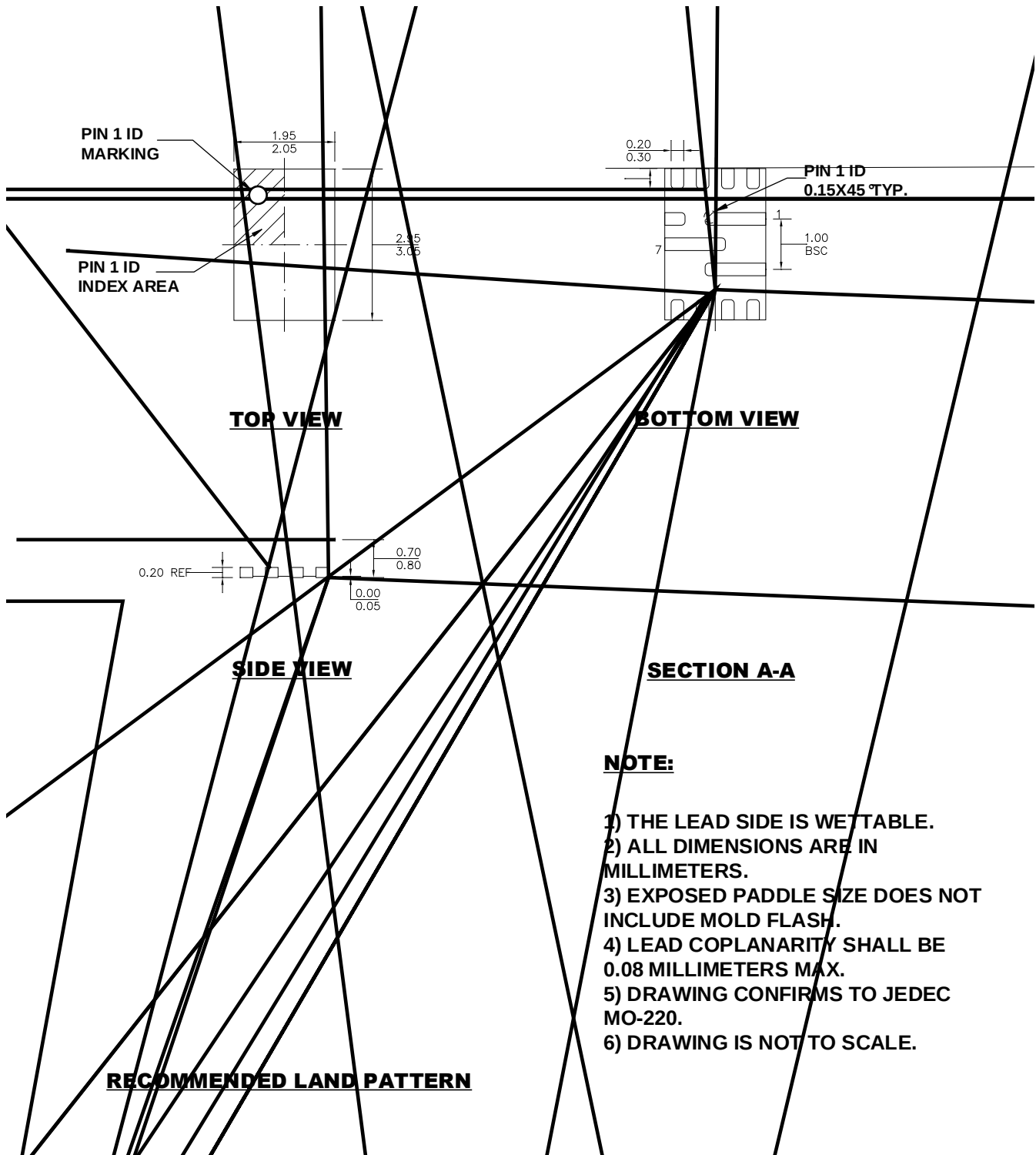
, %

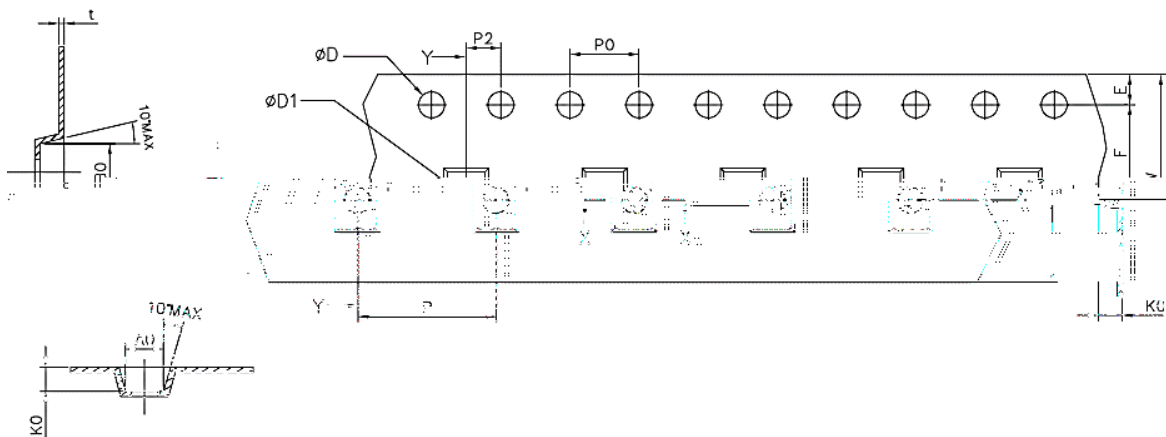
%

- % %



(%





SYMBOL	MILLIMETER		
	MIN	NOM	MAX
E	(%,	(%,	(%,
F	, %,	, %	, %,
P2	(%,	) %	) %,
D		(%	(%
D1		(%	(%,
P0	%	+%	+%
W	((%	() %	() %
P	. %	/ %	/ %
A0	) %	) %	) %
B0	%	%	%
K0	(%+	(%+	(%+
t	%	%,	%.