

## 60V Input, 100mA Output Current, Low Quiescent Current LDO

### FEATURES

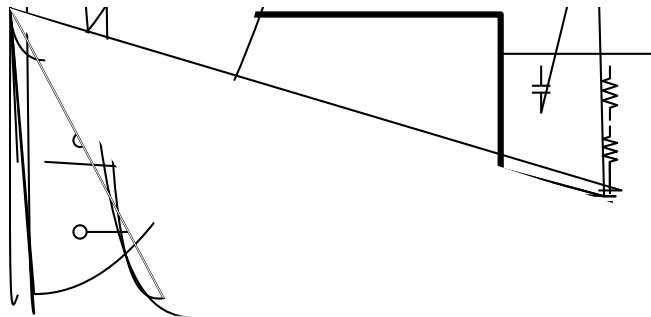
### DESCRIPTION

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### APPLICATIONS

### TYPICAL APPLICATION



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**REVISION HISTORY**

**DEVICE ORDER INFORMATION**

PIN CONFIGURATION



PIN FUNCTIONS

EMSOP-8L/SCT71601A00Q/ SCT71601FxxQ:

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RECOMMENDED OPERATING CONDITIONS

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## ABSOLUTE MAXIMUM RATINGS

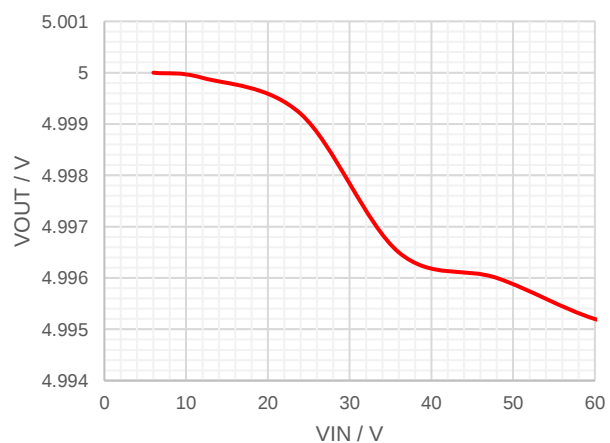
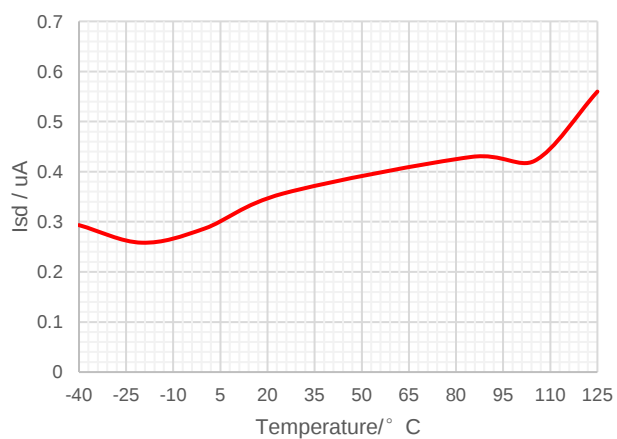
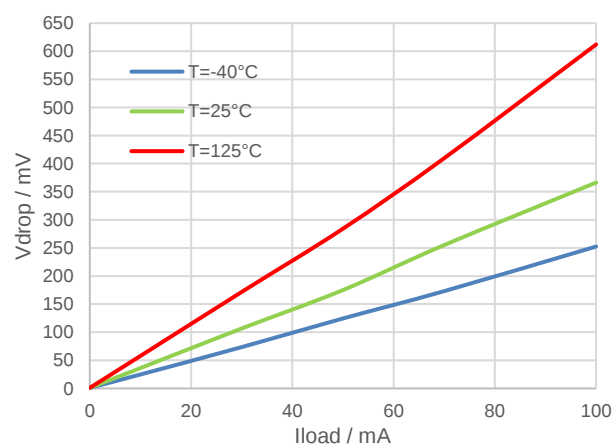
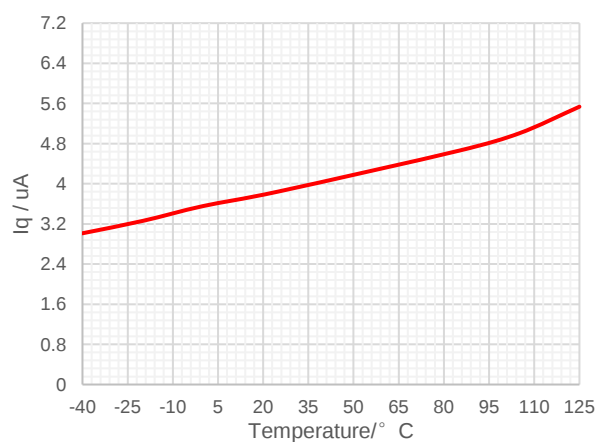
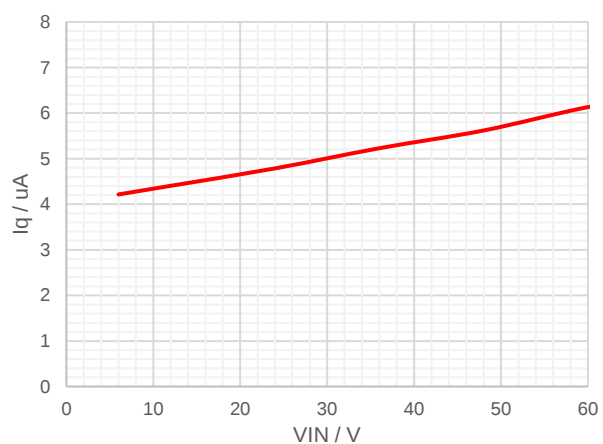
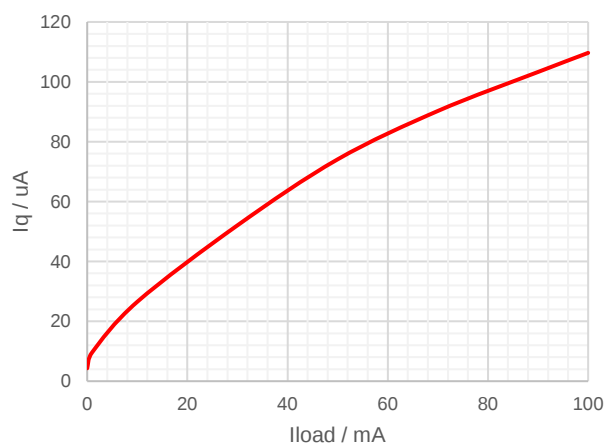
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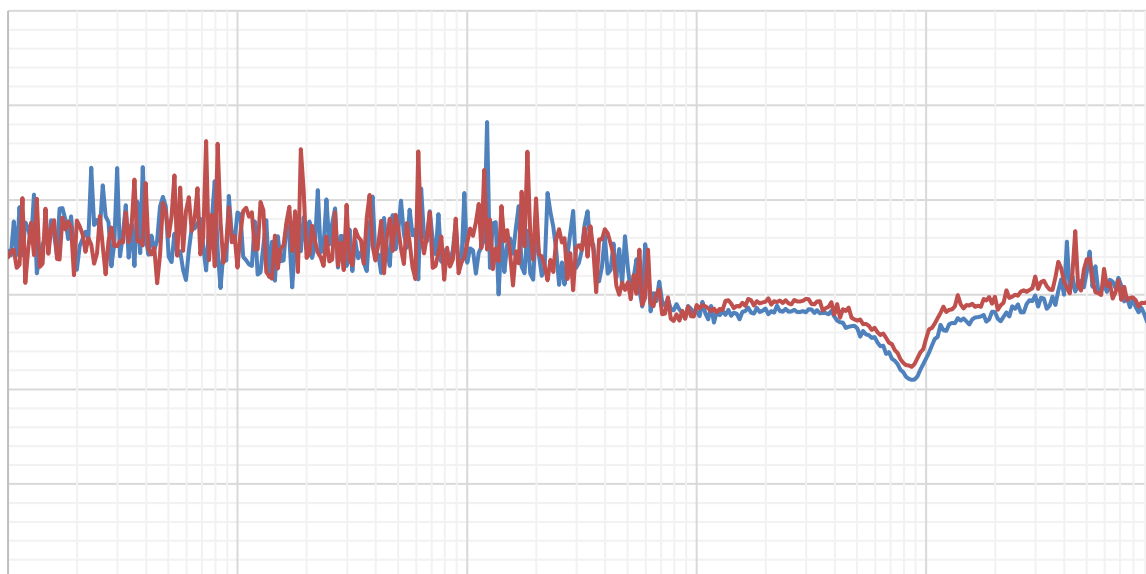
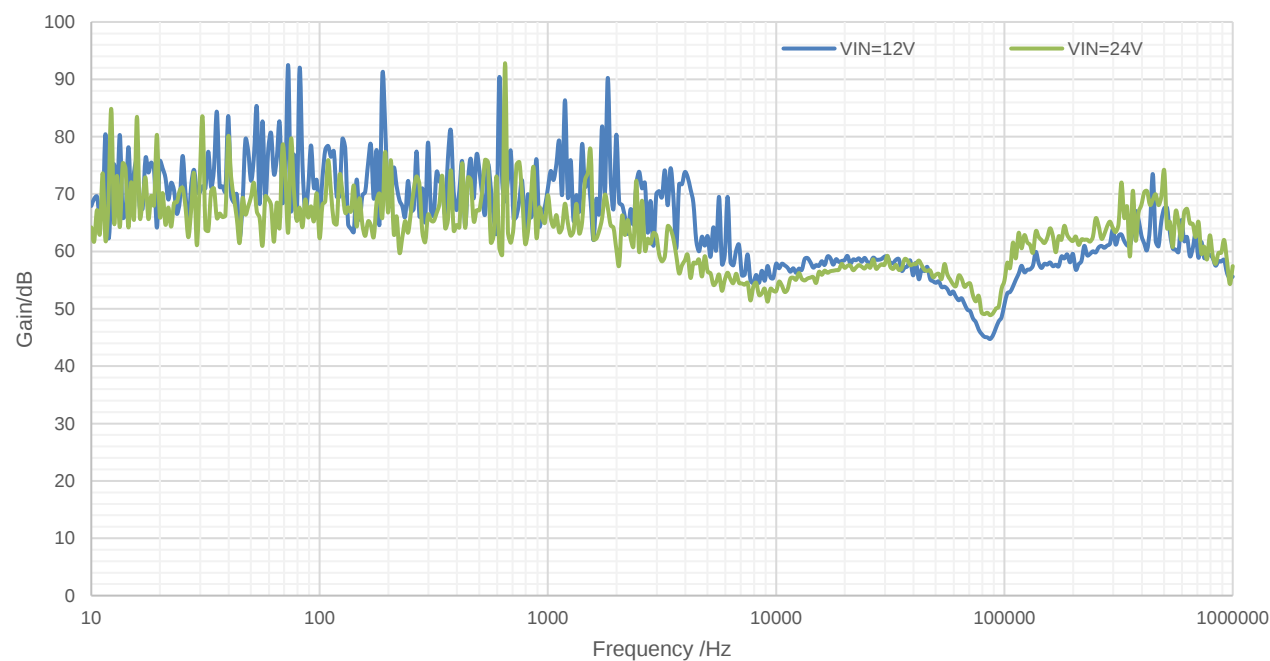
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## TYPICAL CHARACTERISTICS

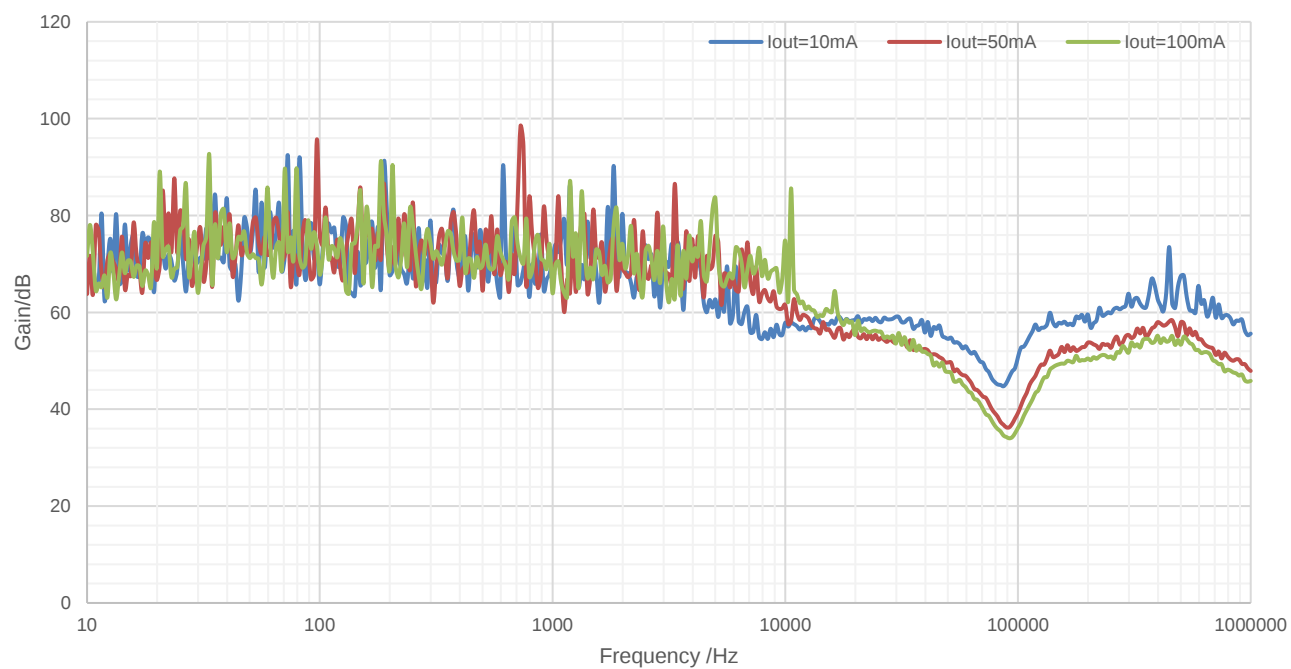
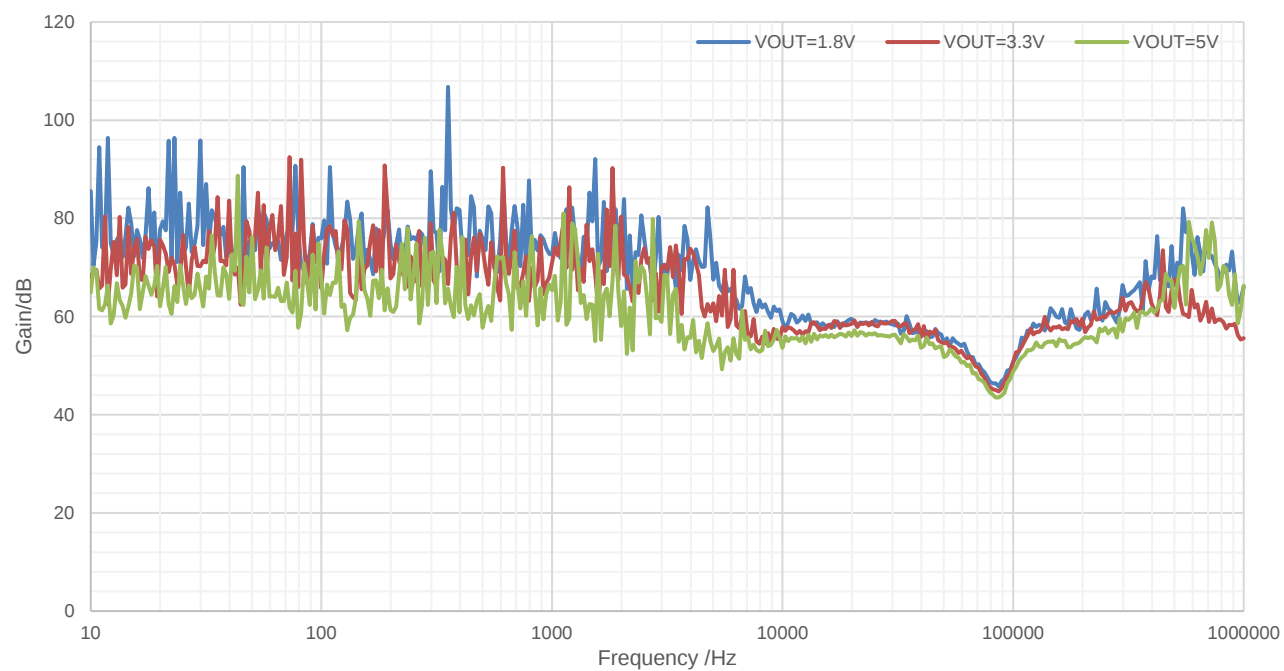


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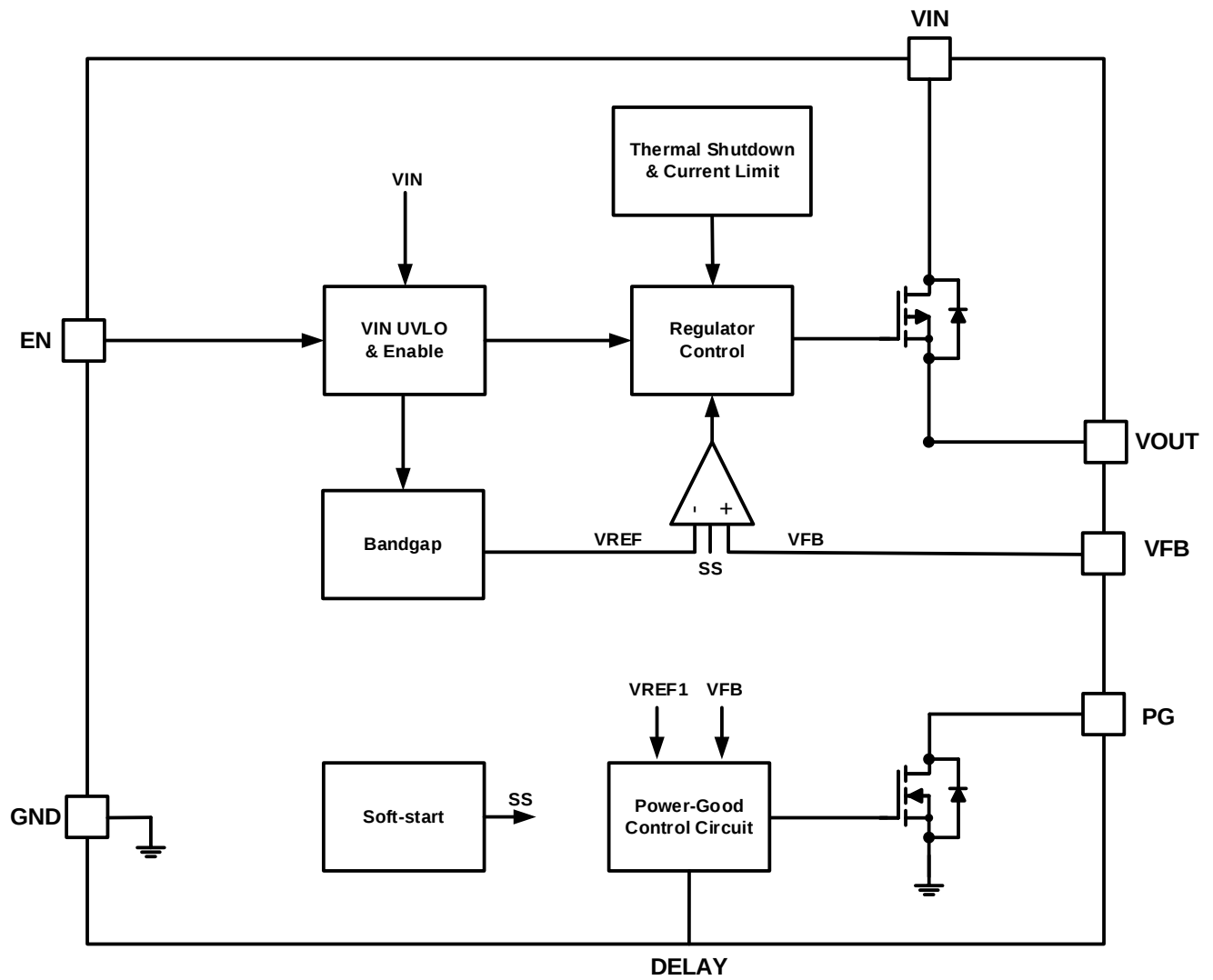
# TYPICAL CHARACTERISTICS (continued)



## TYPICAL CHARACTERISTICS (continued)



## FUNCTIONAL BLOCK DIAGRAM



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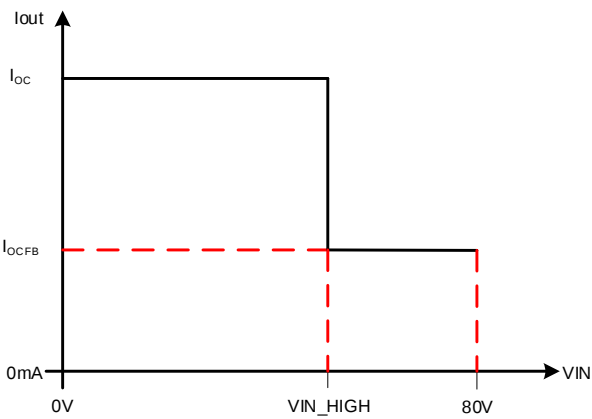
# OPERATION

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R Y H U R Y S H U M o ) P R B 9 ' 7 R b S ^ # 6 ± Í > ± € + + ÿ 9 - ñ ` R Y M H P U Y 9 H ' B ! ! u

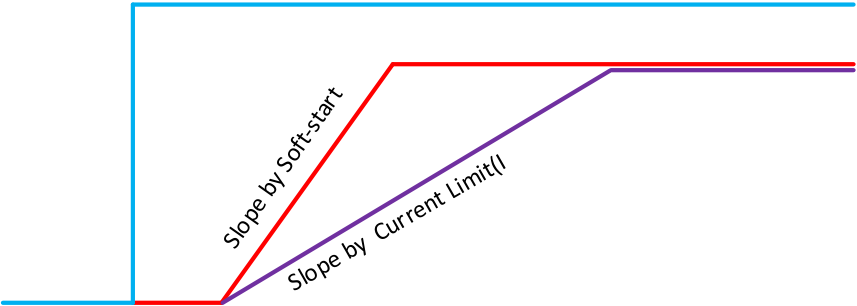
VIN\_HIGH Control



VIN\_HIGH

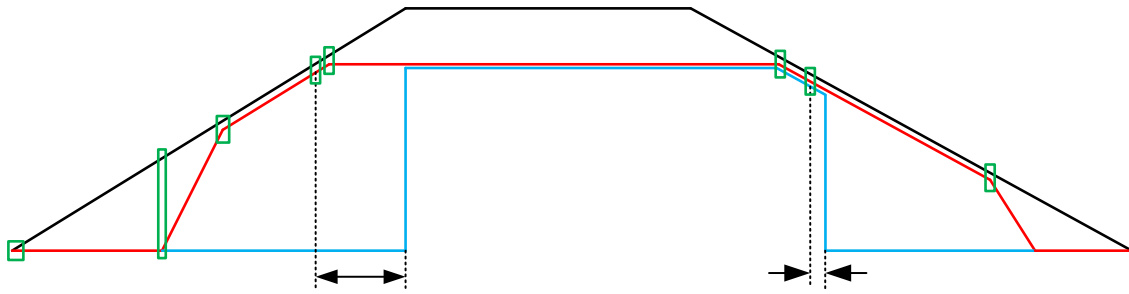
VIN\_HIGH

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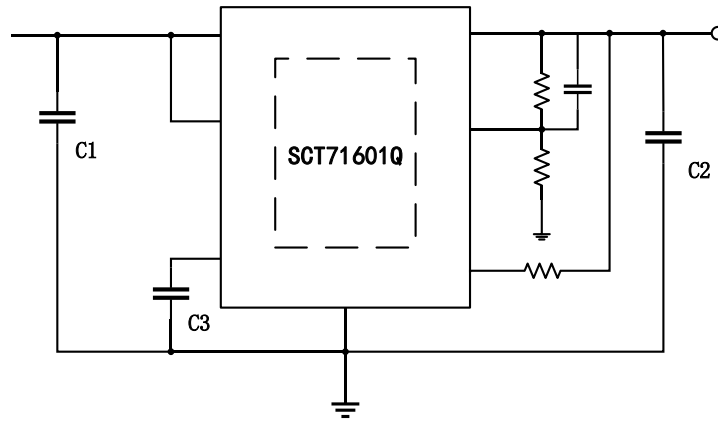


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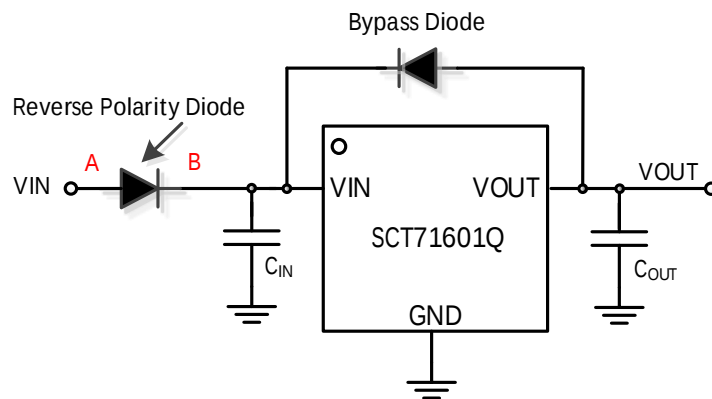
. When  $C_{DELAY}$  is used, the PG output is high-impedance when  $V_{OUT}$  exceeds  $V_{IT}$ , and  $V_{DELAY}$  exceeds  $V_{REF}$ . The power-good delay time can be calculated using:  $t_{DELAY} = (C_{DELAY} \times V_{REF}) / I_{DELAY}$ . For example, when  $C_{DELAY} = 10 \text{ nF}$ , the PG delay time is approximately 12 ms; that is,  $(10 \text{ nF} \times 1.2\text{V}) / 1 \mu\text{A} = 12 \text{ ms}$ .



## APPLICATION INFORMATION

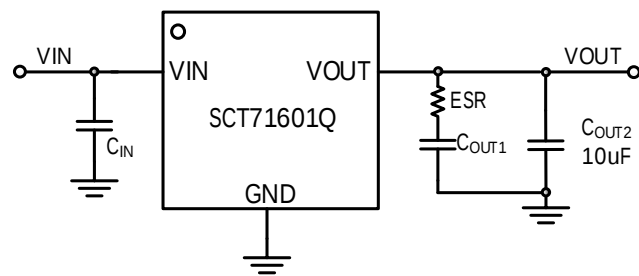


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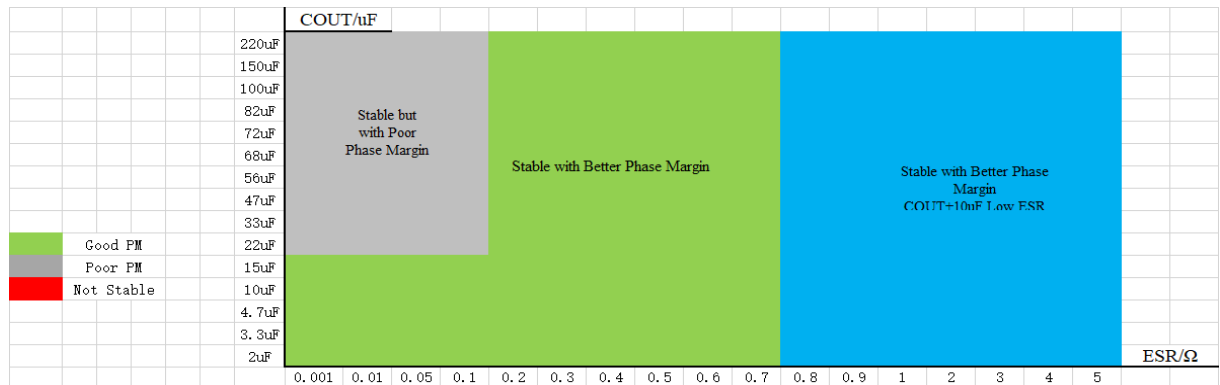
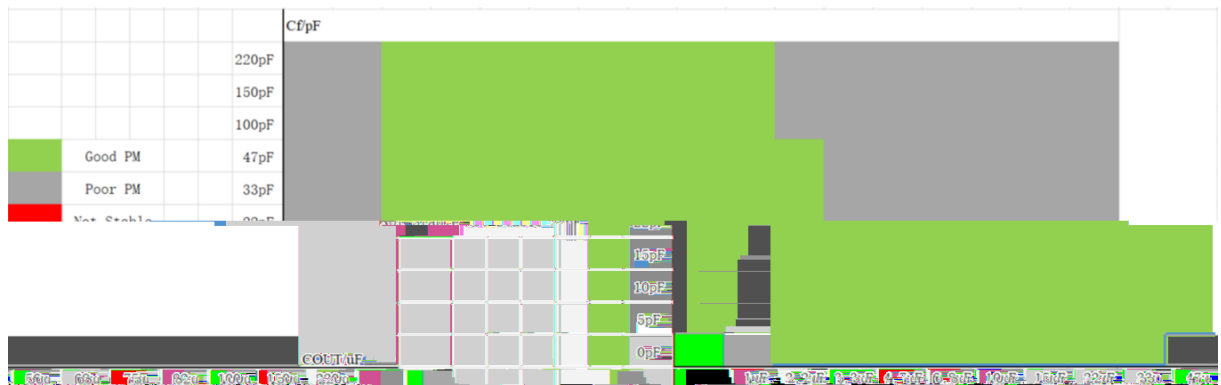
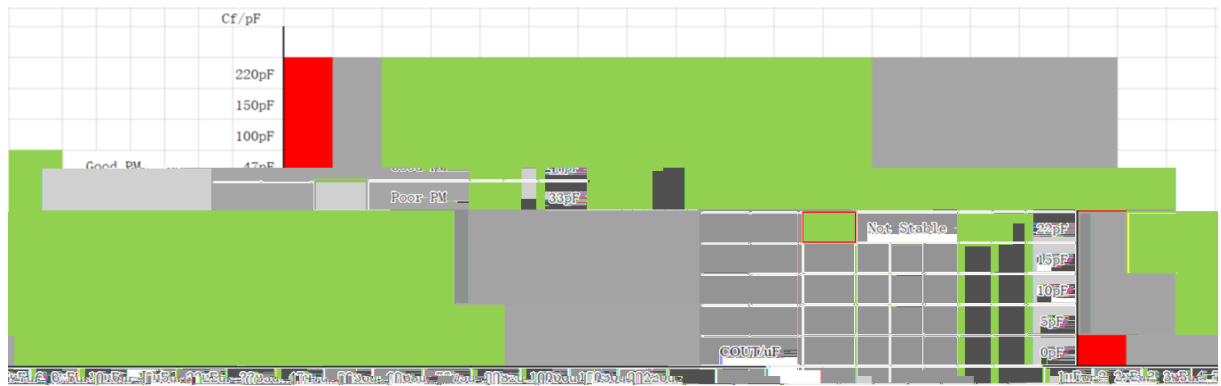
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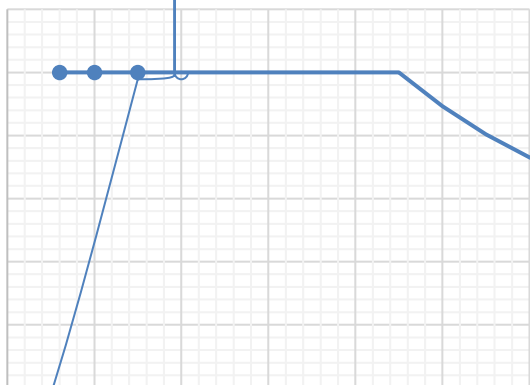
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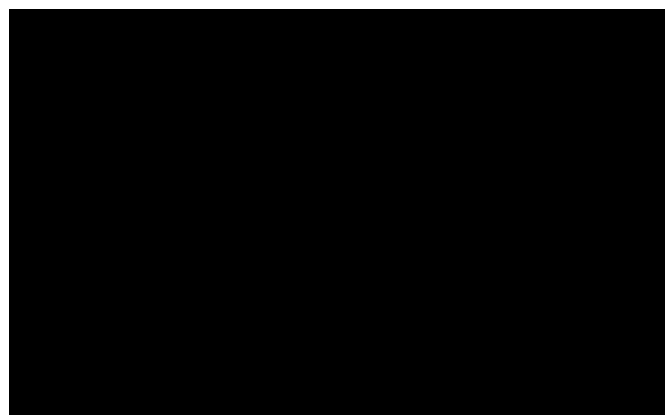
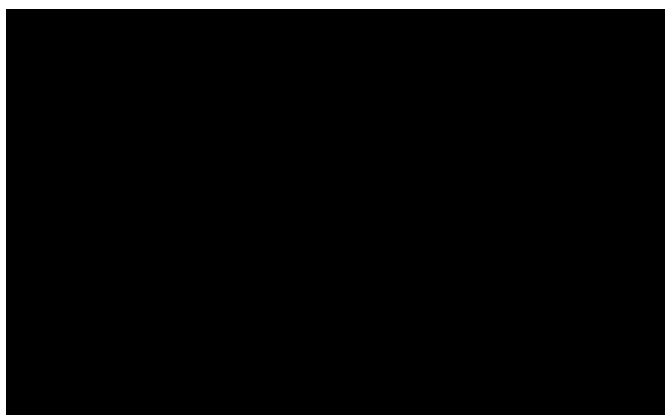
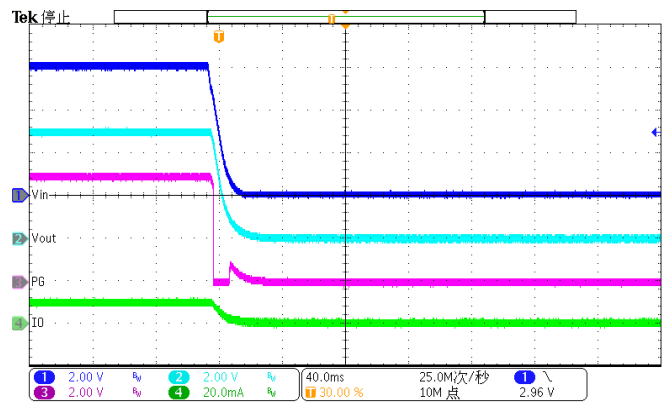
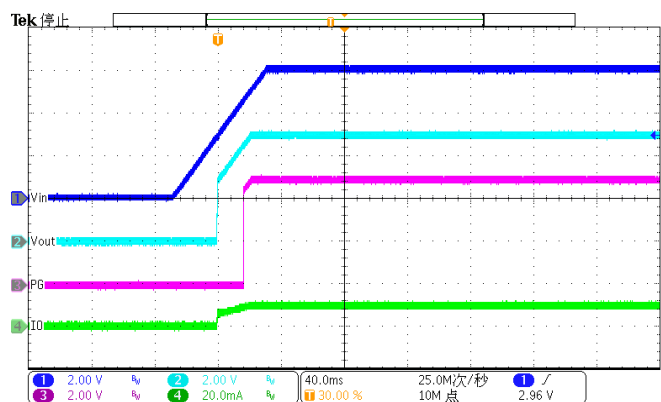
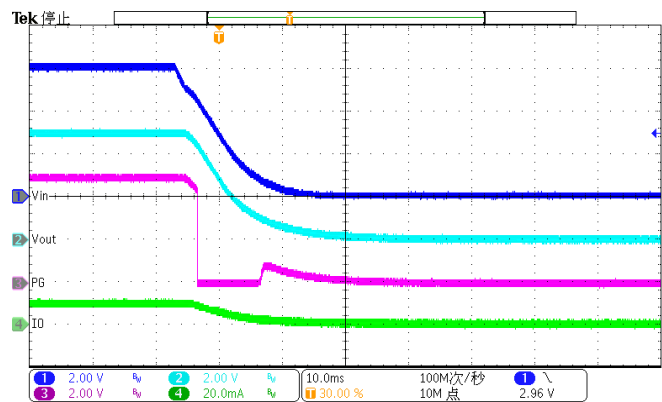
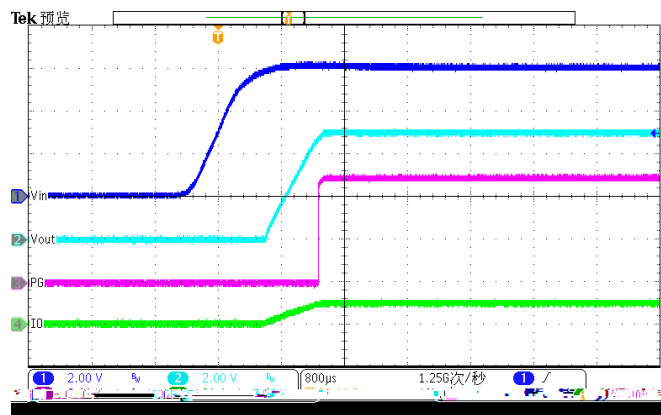
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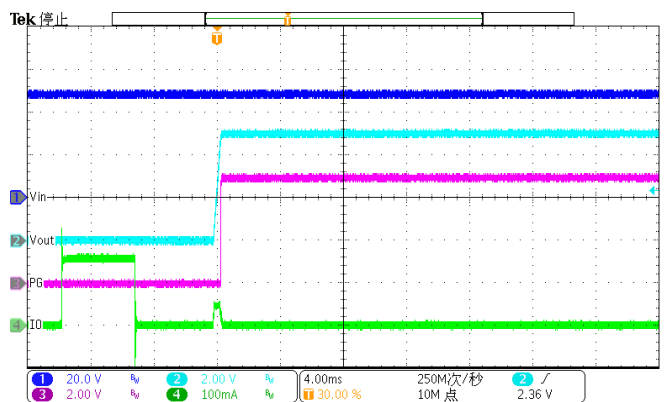
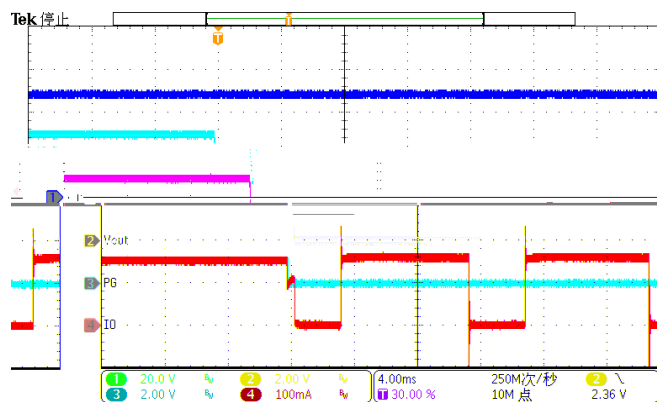
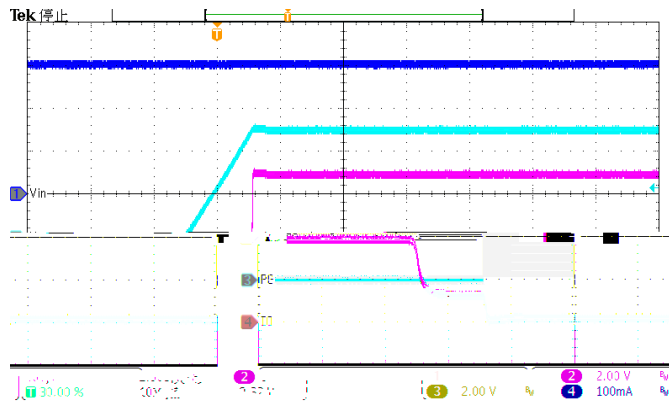
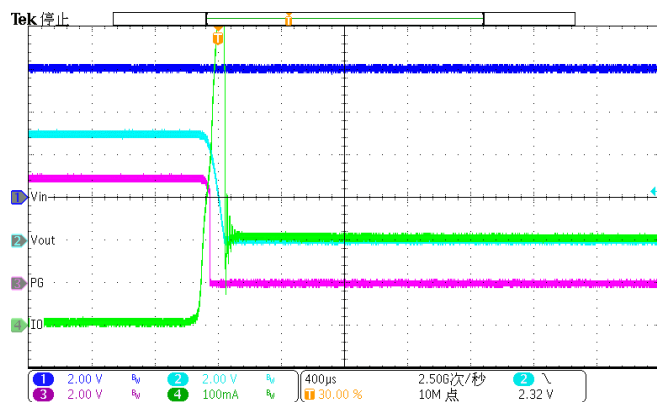
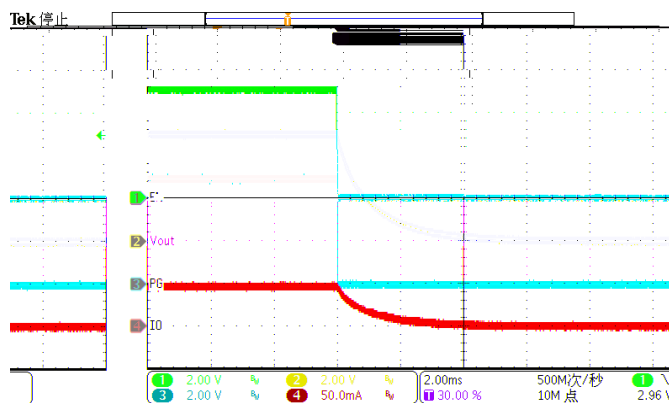
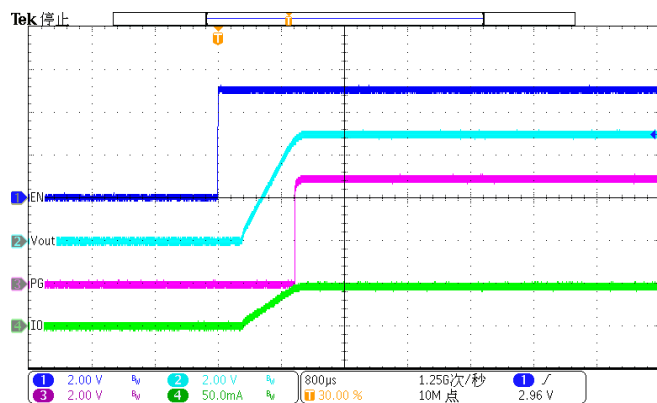
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## THERMAL CHARACTERISTICS

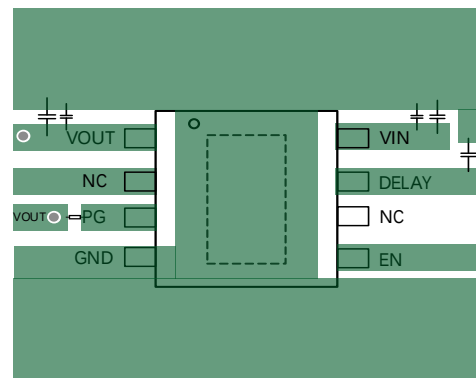
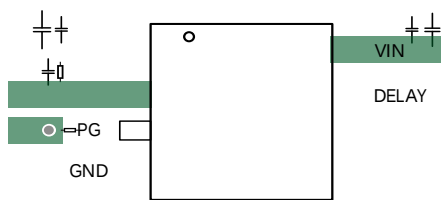


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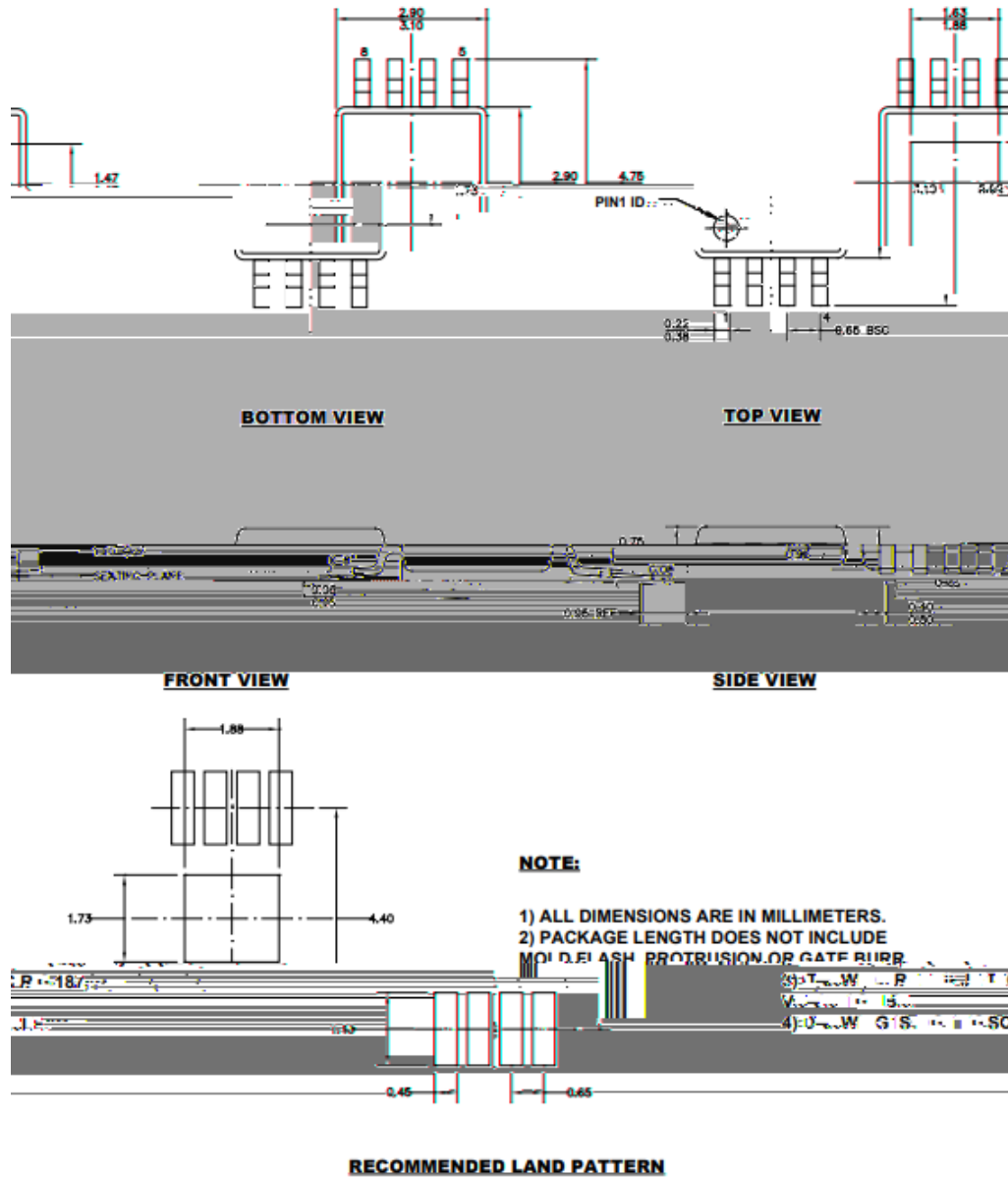


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## PACKAGE INFORMATION

### PACKAGE OUTLINE DRAWING FOR MSOP8L-EP POD-0050 Revision 0.0



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## TAPE AND REEL INFORMATION

