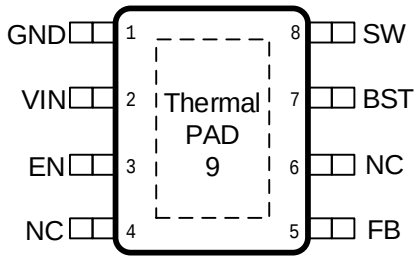
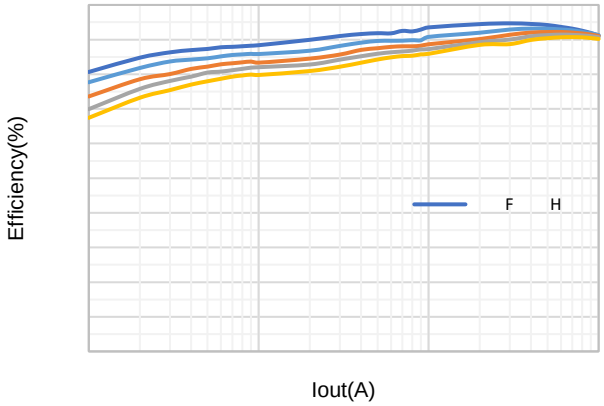


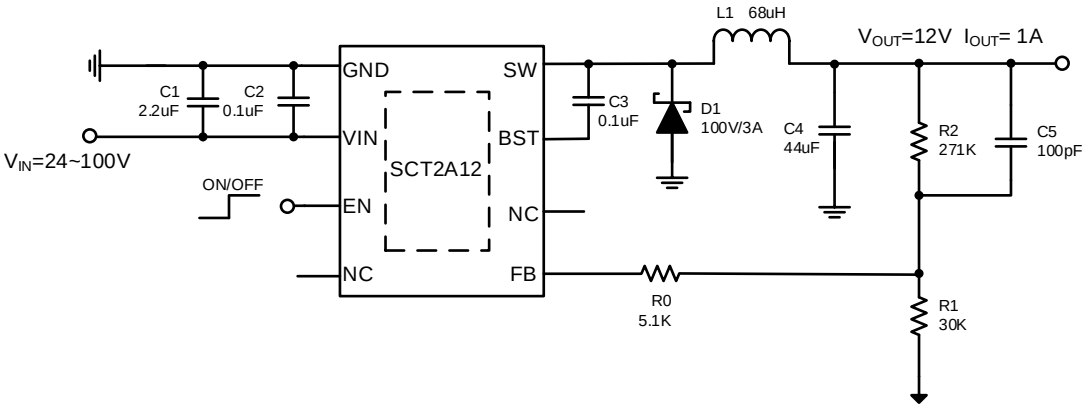
(1)

(2)
(3)


Efficiency(%)
F
H
Iout(A)

x

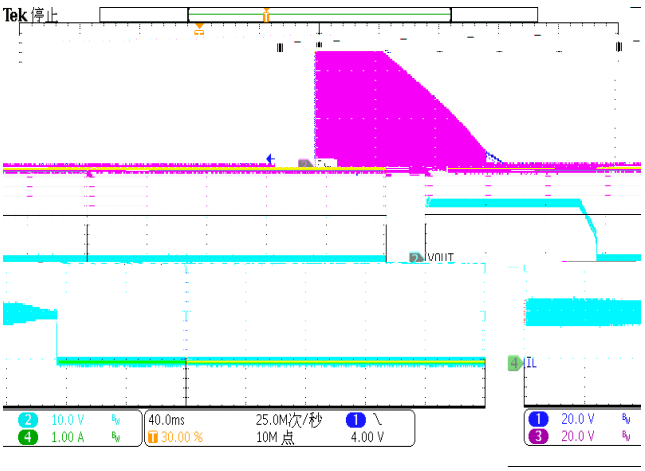
x

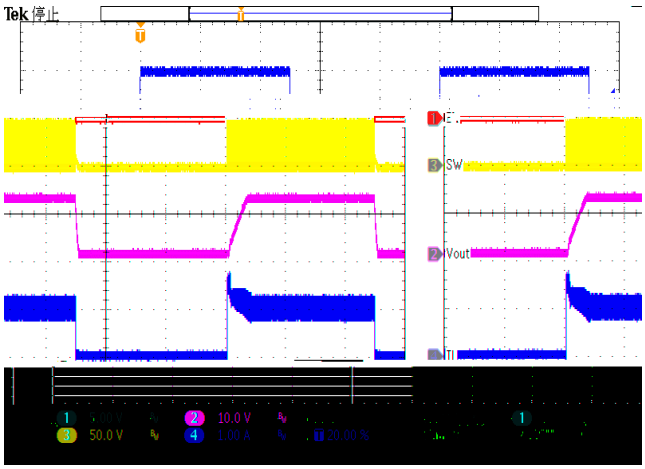

L1 68uH
V_{OUT}=12V I_{OUT}= 1A
GND
SW
C1
2.2uF
C2
0.1uF
C3
0.1uF
D1
100V/3A
C4
44uF
R2
271K
C5
100pF
VIN
BST
V_{IN}=24~100V
SCT2A12
ON/OFF
EN
NC
NC
FB
R0
5.1K
R1
30K

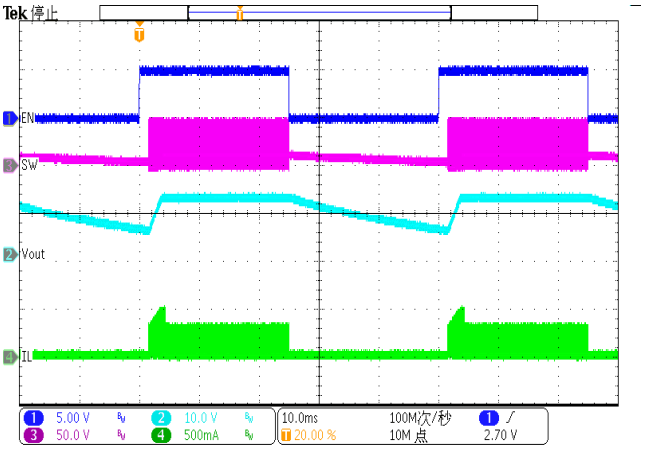
x
x

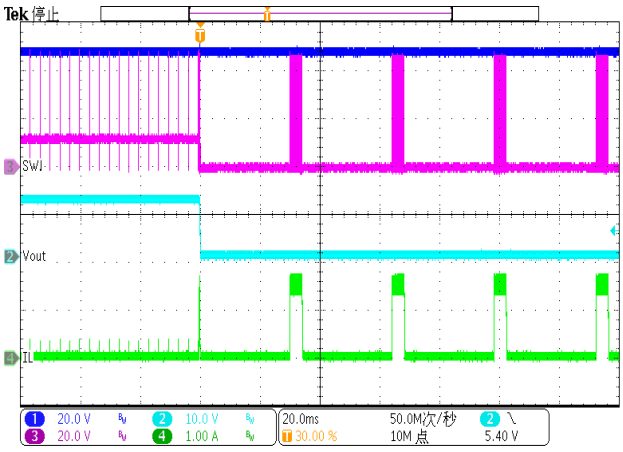
x
x
x
x
x
x

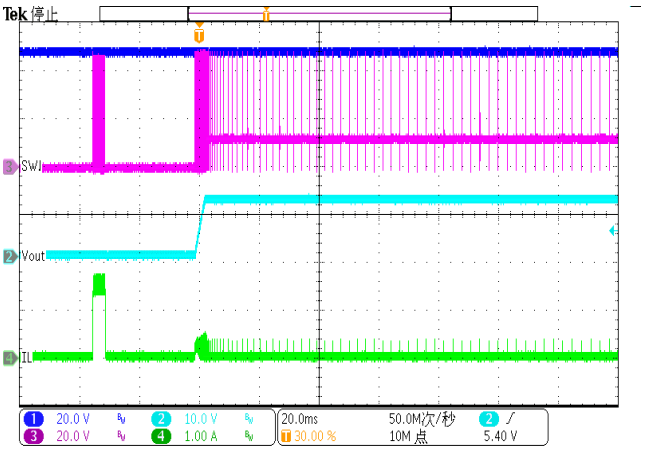
		H N			

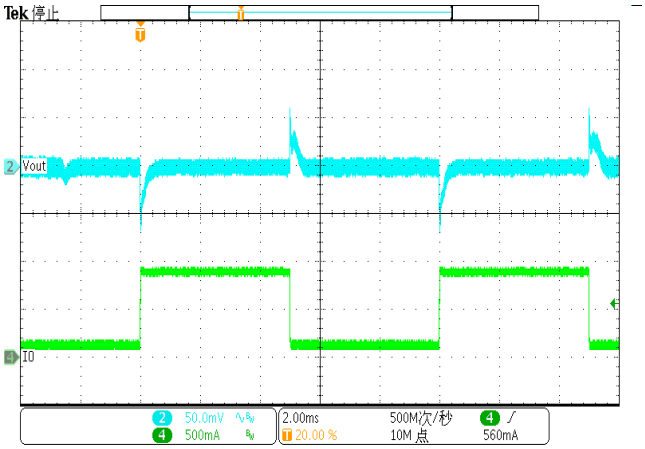

Tek 停止
VOUT
IL
2 10.0 V
4 1.00 A
40.0ms
30.00 %
25.0M次/秒
10M 点
1 \
4.00 V
1 20.0 V
3 20.0 V

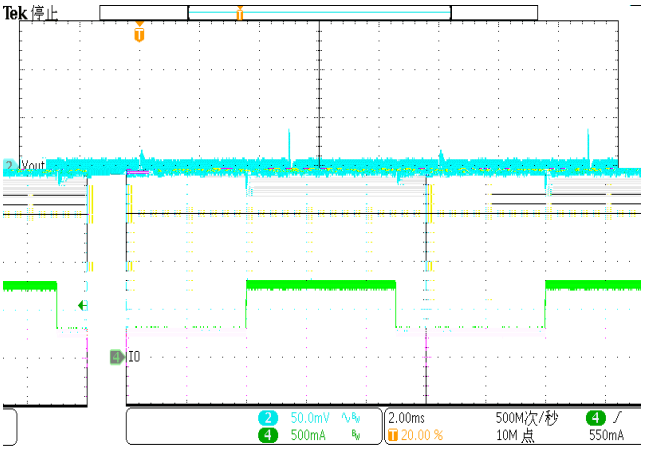

Tek 停止
EN
SW
Vout
IL
1 5.00 V
3 50.0 V
2 10.0 V
4 1.00 A
20.00 %

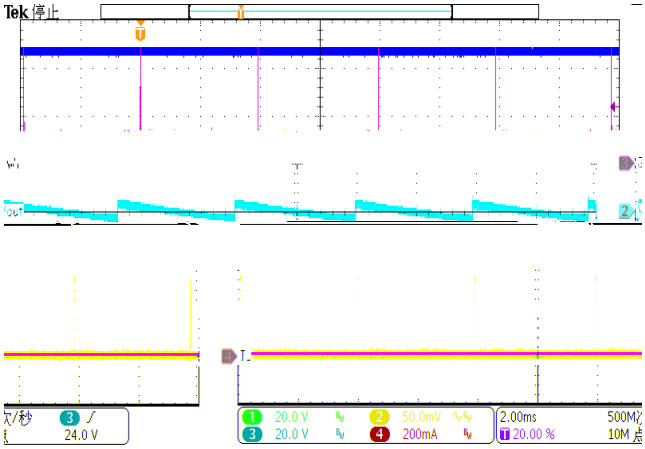

Tek 停止
EN
SW
Vout
IL
1 5.00 V
3 50.0 V
2 10.0 V
4 500mA
10.0ms
20.00 %
100M次/秒
10M 点
1 /
2.70 V

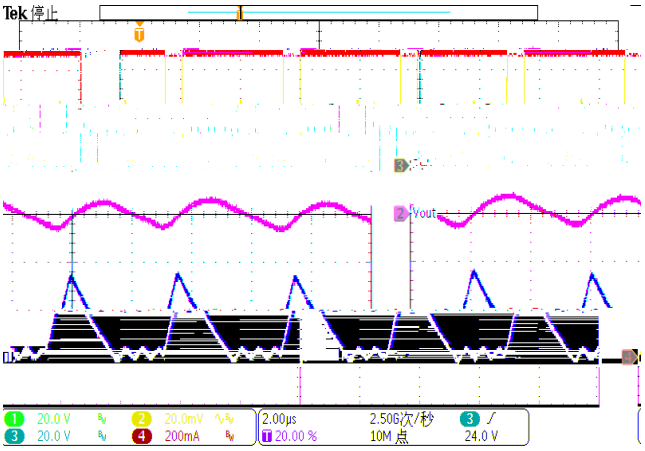

Tek 停止
SW1
Vout
IL
1 20.0 V
3 20.0 V
2 10.0 V
4 1.00 A
20.0ms
30.00 %
50.0M次/秒
10M 点
2 \
5.40 V

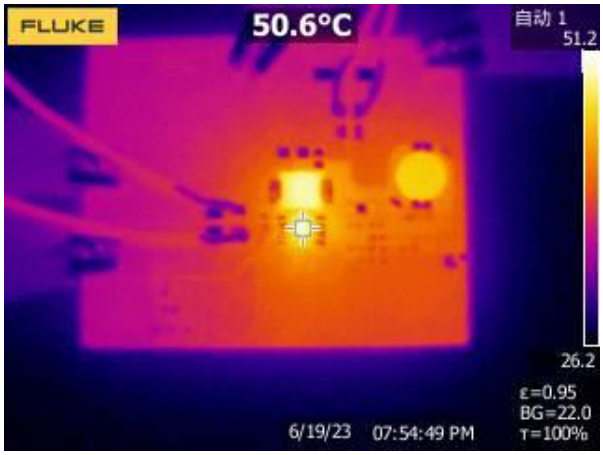

Tek 停止
SW1
Vout
IL
1 20.0 V
3 20.0 V
2 10.0 V
4 1.00 A
20.0ms
30.00 %
50.0M次/秒
10M 点
2 /
5.40 V


Tek 停止
Vout
IO
50.0mV
500mA
2.00ms
20.00 %
500M次/秒
10M 点
560mA


Tek 停止
Vout
IO
50.0mV
500mA
2.00ms
20.00 %
500M次/秒
10M 点
550mA


Tek 停止
24.0 V
20.0 V
20.0 V
50.0mV
200mA
2.00ms
20.00 %


Tek 停止
Vout
20.0 V
20.0 V
20.0mV
200mA
2.00µs
20.00 %
2.50G次/秒
10M 点
24.0 V


FLUKE
50.6°C
自动 1
51.2
26.2
ε=0.95
BG=22.0
T=100%
6/19/23 07:54:49 PM

1.

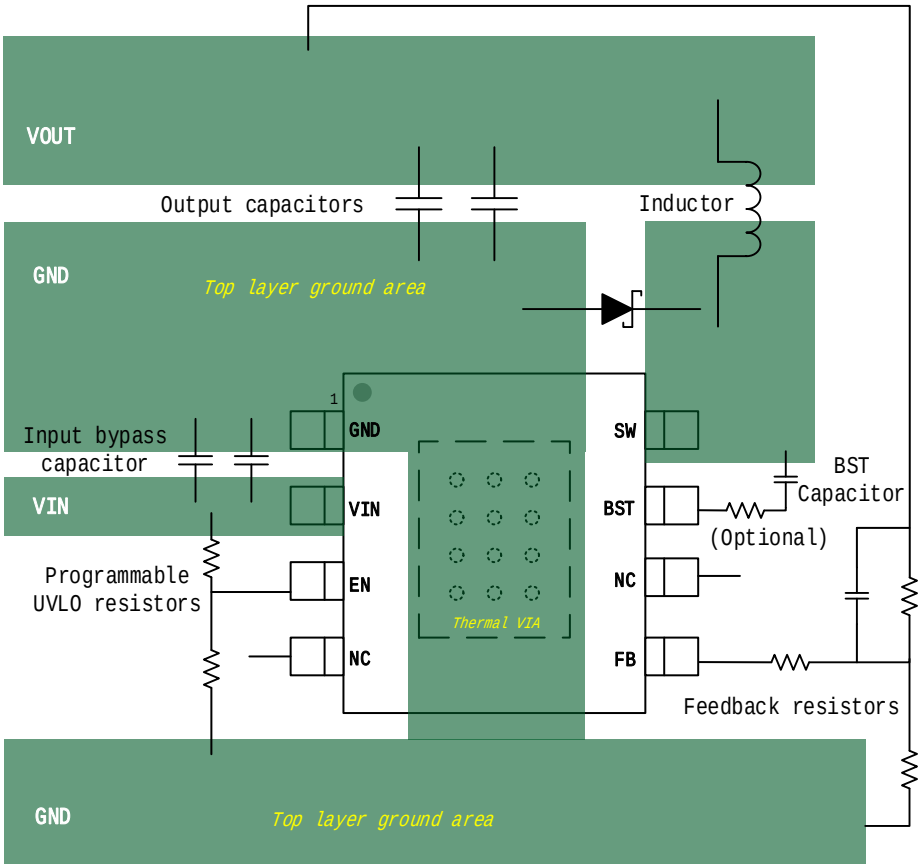
2.

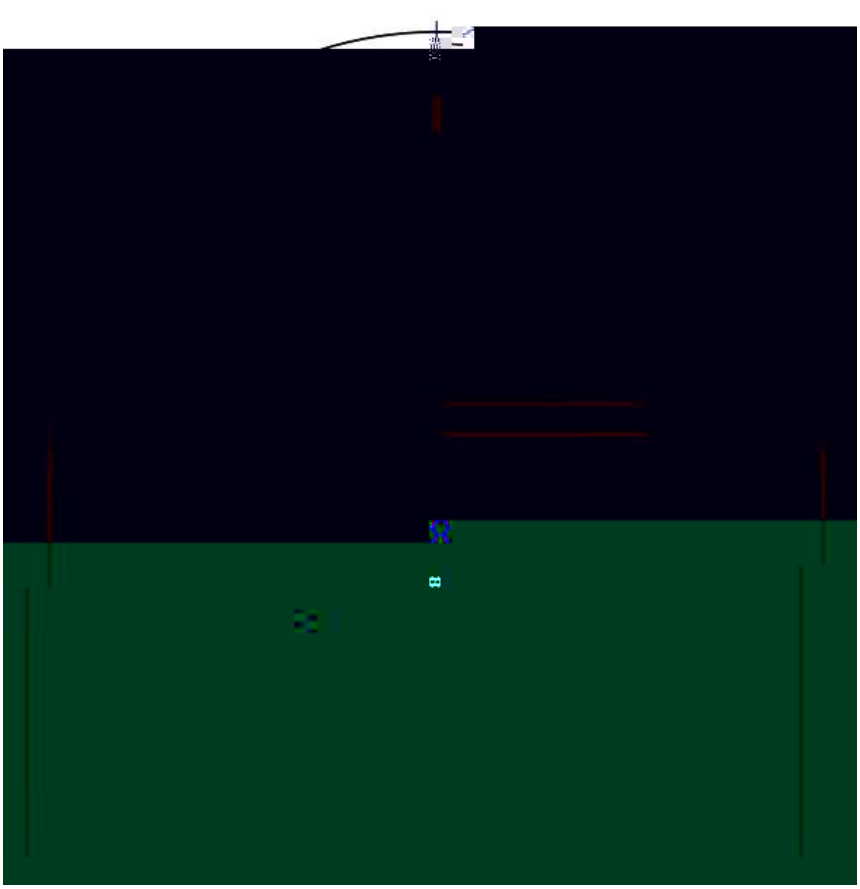
3.

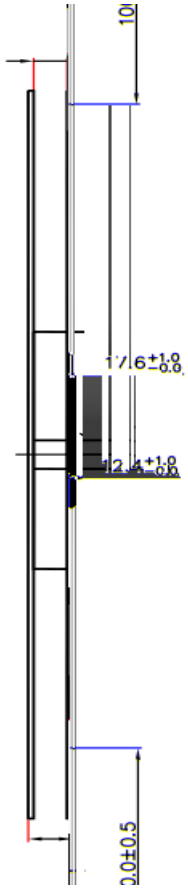
4.

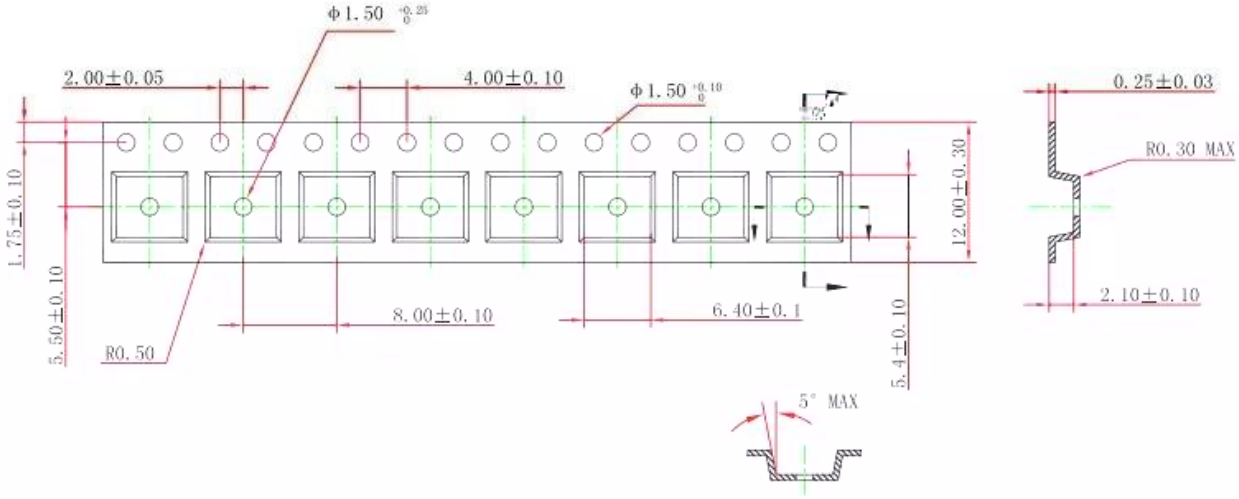
5.

6.


VOUT
Output capacitors
Inductor
GND
Top layer ground area
1
GND
SW
Input bypass
capacitor
VIN
VIN
BST
BST
Capacitor
(Optional)
Programmable
UVLO resistors
EN
NC
Thermal VIA
NC
FB
Feedback resistors
GND
Top layer ground area






Φ1.50 +0.25 0
2.00±0.05
4.00±0.10
Φ1.50 +0.10 0
1.75±0.10
5.50±0.10
R0.50
8.00±0.10
6.40±0.1
12.00±0.30
5.4±0.10
5° MAX
0.25±0.03
R0.30 MAX
2.10±0.10